
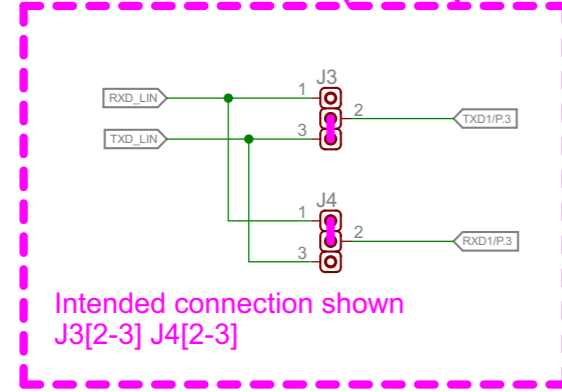


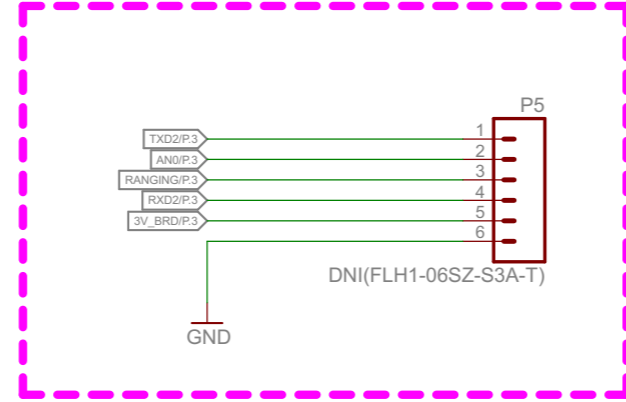
DATE	DESCRIPTION	REV
2015.03.20	Added jumper descriptions	1370
2014.07.03	Release	918
2014.06.12	Preliminary	0
Revision History		

APPROVALS		 Engineering Technologies Canada Ltd. 902-628-1705 www.engtech.ca	SIZE
DRAWN	DATE		Drawing_Rev: LINTST-1173 Copyright (c) 2015 Engineering Technologies Canada Ltd. Title: Block diagram
CHECKED	DATE	20x15	
FINAL	DATE		
		Date: 4/22/2015 9:46:13 AM	Sheet: 1/3

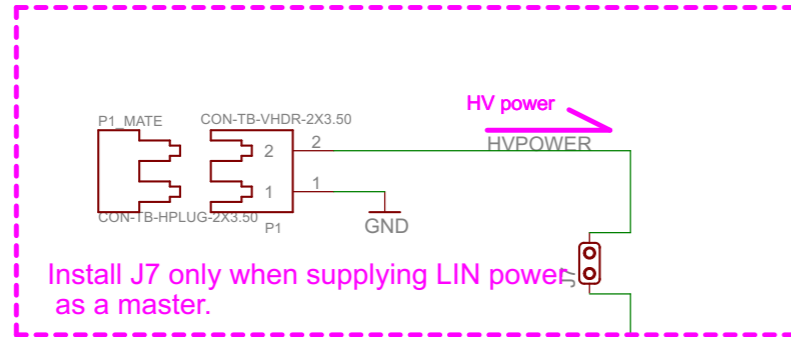
TX/RX Select (temporary)



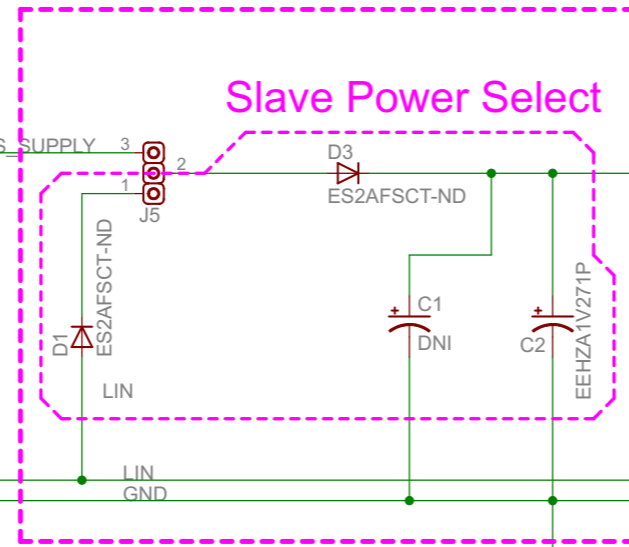
Sensor Interface



Master ONLY

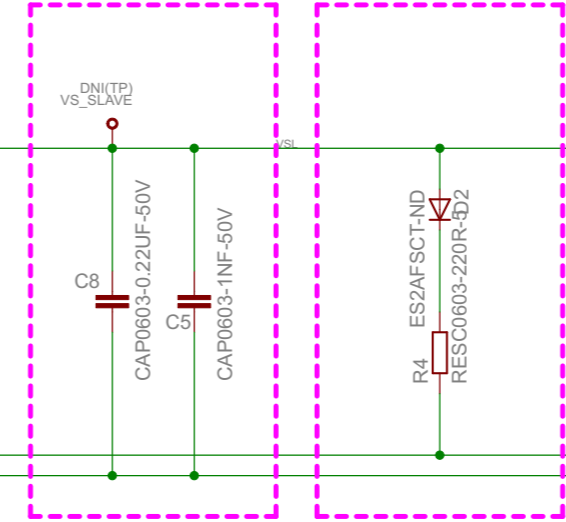


SLAVE

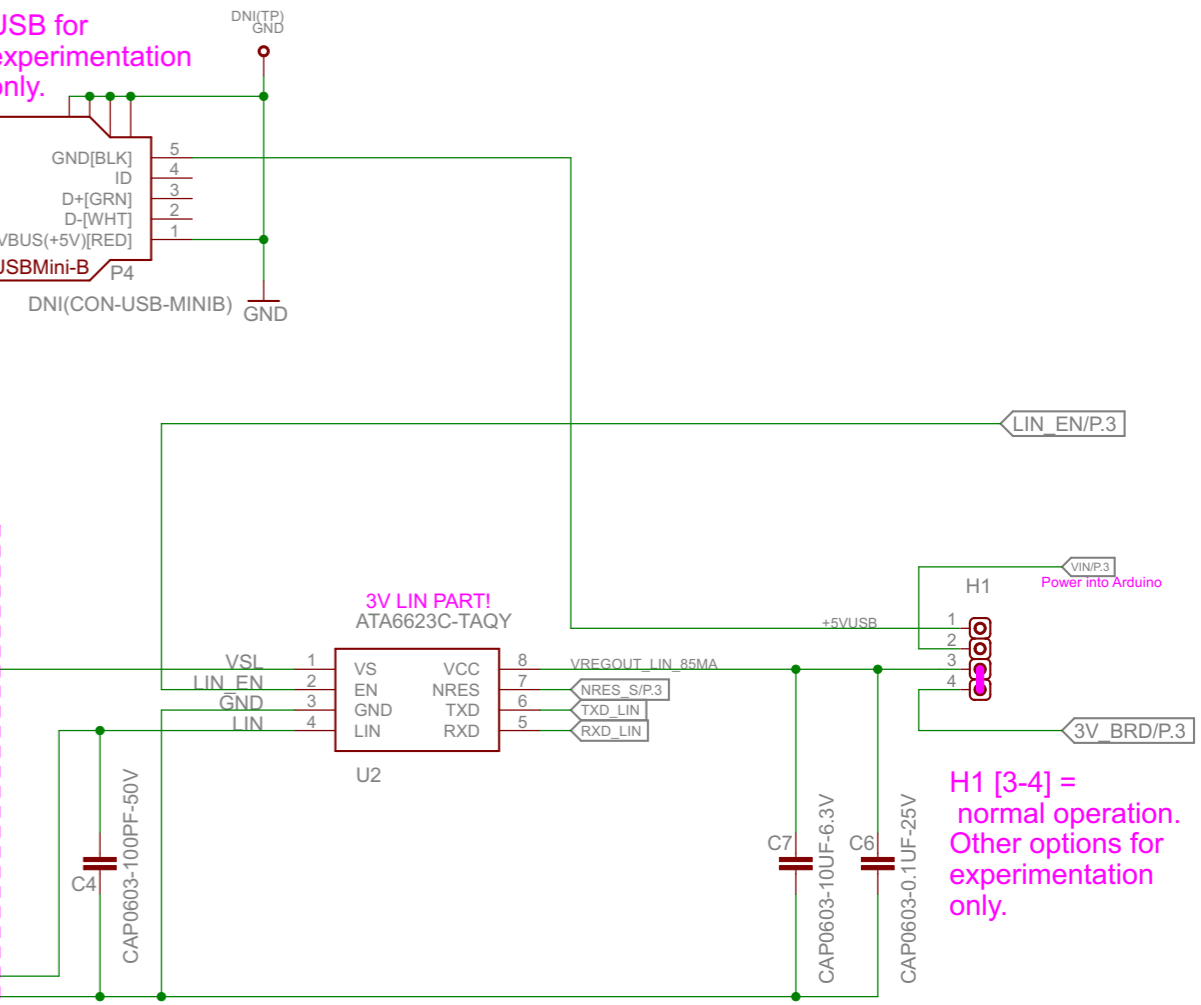


J5 [2-3] for normal 3 wire operation.
 J5 [1-2] for using special two wire implementation where C1 and C2 continue to supply power and keep low power microcontroller operating while LIN Data bus is toggling.
 See Atmel app note for details.

Slave only? Filtering Caps dependent on application



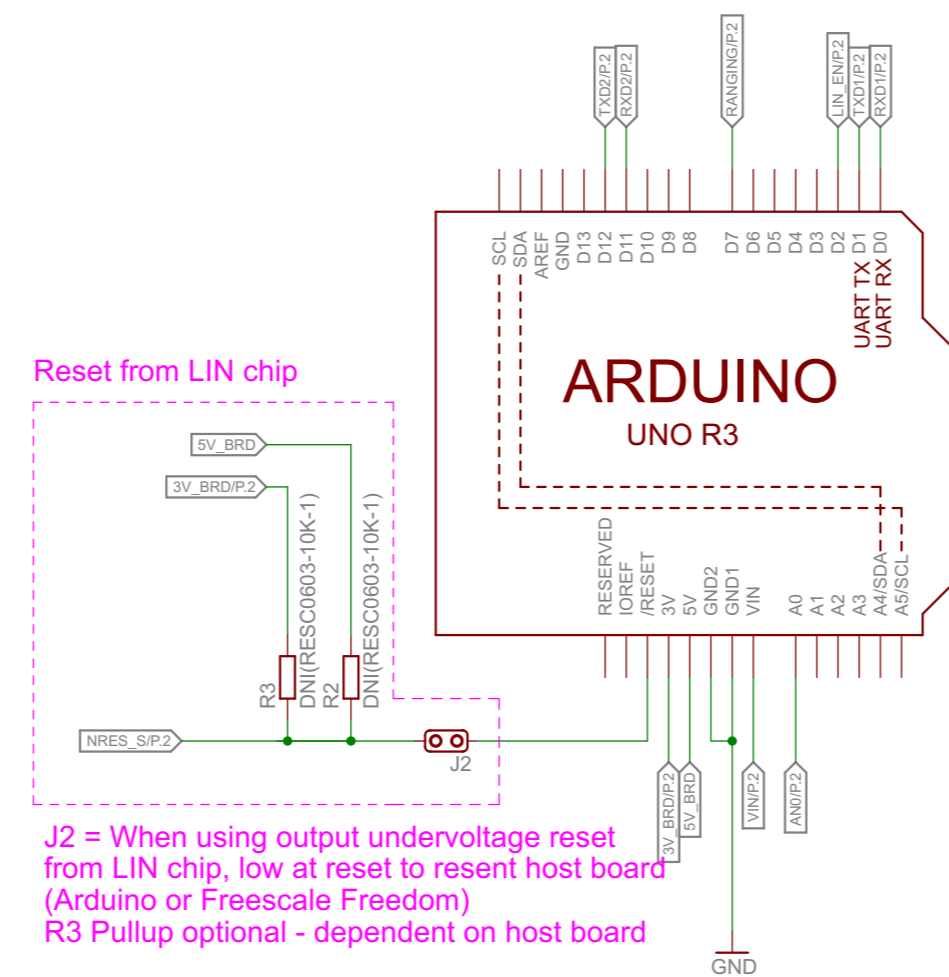
Master Node Pull-up



H1 [3-4] = normal operation. Other options for experimentation only.

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DRAWN IAFG	DATE Mar-2014		
CHECKED	DATE		
FINAL	DATE		



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		Date: 4/22/2015 9:46:13 AM	Sheet: 3/3